

Power management (dual transistors)

VT6T2

Structure

PNP silicon epitaxial planar transistor

Features

Very small package with two transistors.

Applications

Switch, LED driver

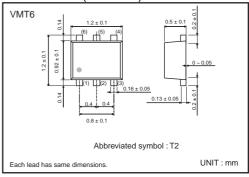
Packaging specifications

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	Package	Taping			
	Code	T2R			
Туре	Basic ordering unit (pieces)	8000			
VT6T2		0			

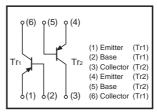
Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Collector-base voltage		Vсво	-50	V	
Collector-emitter voltage		Vceo	-50	V	
Emitter-base voltage		Vево	- 5	V	
Collector current		Ic	-100	mA	
		ICP *1	-200	mA	
Power dissipation	Total	Pp *2	150	mW	
	Element	10	120	mW	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

●Dimensions (Unit:mm)



•inner circuit



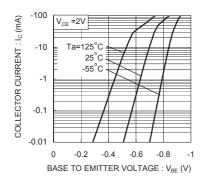
●Electrical characteristics (Ta=25°C)

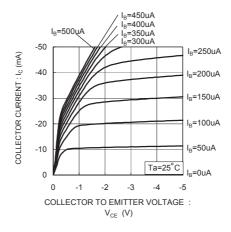
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BVceo	-50	_	_	V	Ic=-1mA
Collector-base breakdown voltage	ВУсво	-50	_	_	V	Ic= -50μA
Emitter-base breakdown voltage	ВУево	-5	_	_	V	I∈= −50μA
Collector cut-off current	Ісво	_	_	-0.1	μΑ	Vcb= -50V
Emitter cut-off current	ІЕВО	_	_	-0.1	μΑ	V _{EB} = -5V
Collector-emitter saturation voltage	VCE(sat)	_	-0.15	-0.40	V	Ic= -50mA, I _B = -5mA
DC current gain	hfe	120	_	560	_	Vce= -6V, Ic= -1mA
Transition frequency	f⊤	_	300	_	MHz	Vc=-10V, I=10mA, f=100MHz
Output capacitance	Cob	_	2	_	pF	Vcb= -10V, Ie=0A, f=1MHz

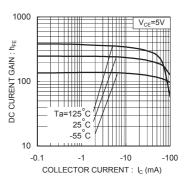
^{*1} Pw=1mS Single pulse *2 Each terminal mounted on a recommended land

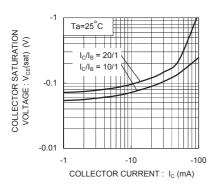
VT6T2 Data Sheet

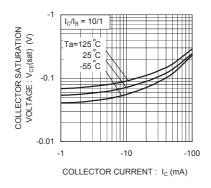
•Electrical characteristics curves

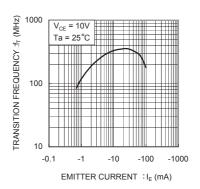


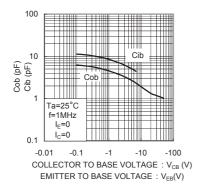












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